IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: Alagarsamy et al.

: Group Art Unit: unknown

Serial No.: unknown

Filed: 11/24/2003

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Title: Binary Coded Decimal Addition

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D. C. 20231

Sir:

Applicants submit herewith patents, publications or other information of which they are aware. which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56 (a) exists.

The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR 1.98 (a)(1).

A copy of each of the items on PTO-1449 is supplied herewith.

Respectfully submitted,

Anthony V.S. England Attorney for Applicants

Registration No. 35,129 (512) 477-7165

				Complete if Known		
Substitute for form 1449/PTO and PTO/SB/08 INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			and PTO/SB/08	Application Number		
				Filing Date		
			APPLICANT	First Named Inventor	Neelamekakannan Alagarsamy	
			ets as necessary)	Art Unit		
				Examiner Name		
Sheet	1	of	2	Attorney Docket No	JP920030216US1	

US PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant	
		Number-Kind Code ^{2 (if known)}			Figures Appear	
		US-4,805,131	Feb. 14, 1989	Adlietta, et al.		
		US-4,866,656	Sep. 12, 1989	Hwang		
		US-4,799,181	Jan. 17, 1989	Tague, et al.		
		US-4,718,033	Jan. 5, 1988	Miller		
		US-4,707,799	Nov. 17, 1987	Ishikawa, et al.		
		US-4,638,300	Jan. 20, 1987	Miller		
		US-4,559,608	Dec. 17, 1985	Young, et al.		
		US-4,172,288	Oct. 23, 1979	Anderson		
		US-4,138,731	Feb. 6, 1979	Kamimoto, et al.		
·········		US-4,001,570	Jan. 4, 1977	Gooding, et al.		
<u> </u>		US-3,935,438	Jan. 27, 1976	Grupe		

	FOREIGN PATENT DOCUMENTS							
Examiner Initials	Cite No.	Foreign Patent Document	Publication Date MM-DD-	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or	6 T		
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	YYYY		Relevant Figures Appear			

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				Application Number		
INFORMATION DICOLOGUES				Filing Date		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			APPLICANT	First Named Inventor	Neelamekakannan Alagarsamy	
	(Use as r	nany s	sheets as necessary)	Art Unit		
				Examiner Name		
Sheet	2	of	2	Attorney Docket No	JP920030216US1	

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No ¹	Include name of Author (in CAPITALS), title of the article, (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
		M. MORRIS MANO and CHARLES R. KIME, Logic and Computer Design Fundamentals, (2001), pp 129-132 and 145-147, second edition, Addison Wesley Longman, New Delhi			
		JOHN F. WAKERLY, Digital Design, Principles and Practices, pp. 441-443, 2001, third edition, Pearson Education Asia, New Delhi			
. 9.0		ZVI KOHAVRI, Switching and Finite Automata Theory, pp. 138-144, 1978, second edition, Tata McGraw Hill, New Delhi			
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